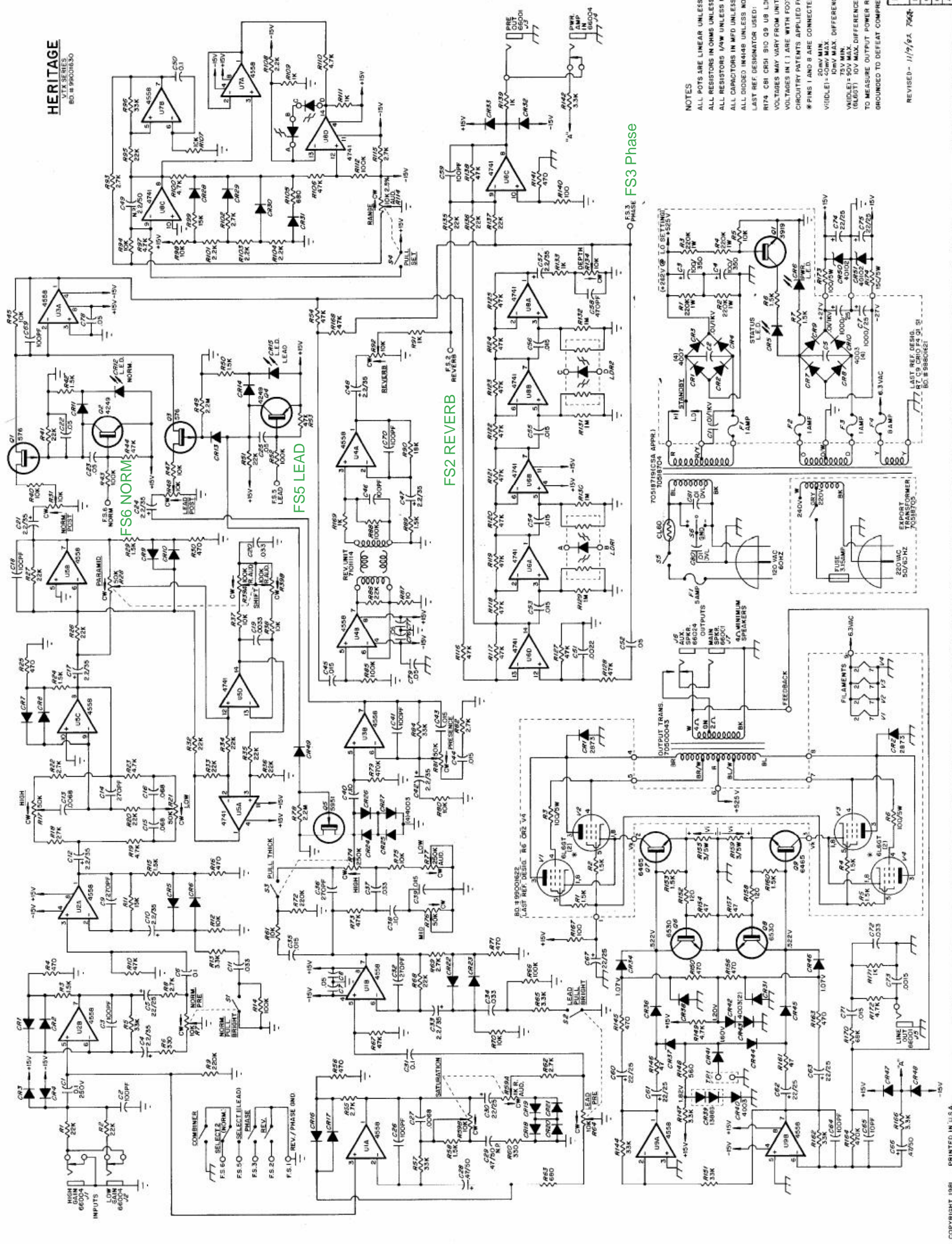


HERITAGE
V.T.X. SERIES
BO. # 99001630



NOTES
ALL POTS ARE LINEAR UNLESS NOTED
ALL RESISTORS IN OHMS UNLESS NOTED
ALL RESISTORS IN μ W UNLESS NOTED
ALL CAPACITORS IN MFD UNLESS NOTED
ALL DIODES IN 1N4148 UNLESS NOTED
LAST REF DESIGNATOR USED:
R174 CBI CR51 910 U8 LDR2 FI J7
VOLTAGES MAY VARY FROM UNIT TO UNIT
VOLTAGES IN () ARE WITH FOOTSWITCH CLOSED
CIRCUITRY PATENTS APPLIED FOR AND PENDING
* PINS 1 AND 8 ARE CONNECTED ON ALL TUBES
V1(V1L1) = 200V MIN.
100V MAX. DIFFERENCE BETWEEN SIDES
V1(V1L1) = 50V MAX.
V1(V1L1) = 10V MAX. DIFFERENCE BETWEEN SIDES
TO MEASURE OUTPUT POWER RATING AT 5% T.D.D., T.F.I. MUST BE
GROUNDED TO DEFEAT COMPRESSION EFFECT OF POWER AMP.

REVISED - 11/19/97 R66
PEAVEY ELECTRONICS CORP.
MERIDIAN, MS.
DRAWN BY R66 DATE 8/25/97
C.B. CHECK R66
CHECK BY R66
APPR. BY R66